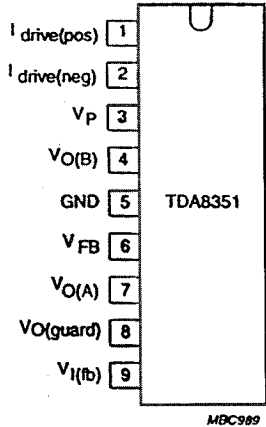


TDA8351

DC-coupled vertical deflection circuit

Pinning

Symbol	Pin	Description
$I_{drive(pos)}$	1	input power-stage(positive); includes $I_{I(sb)}$ signal bias
$I_{drive(neg)}$	2	input power-stage(negative); includes $I_{I(sb)}$ signal bias
V_p	3	operating supply voltage
$V_{O(B)}$	4	output voltage B
GND	5	ground
V_{FB}	6	input flyback supply voltage
$V_{O(A)}$	7	output voltage A
$V_{O(guard)}$	8	guard output voltage
$V_{I(fb)}$	9	input feedback voltage



Metal block connected to substrate pin 5.
Metal on back.

Fig.1 Pin configuration.

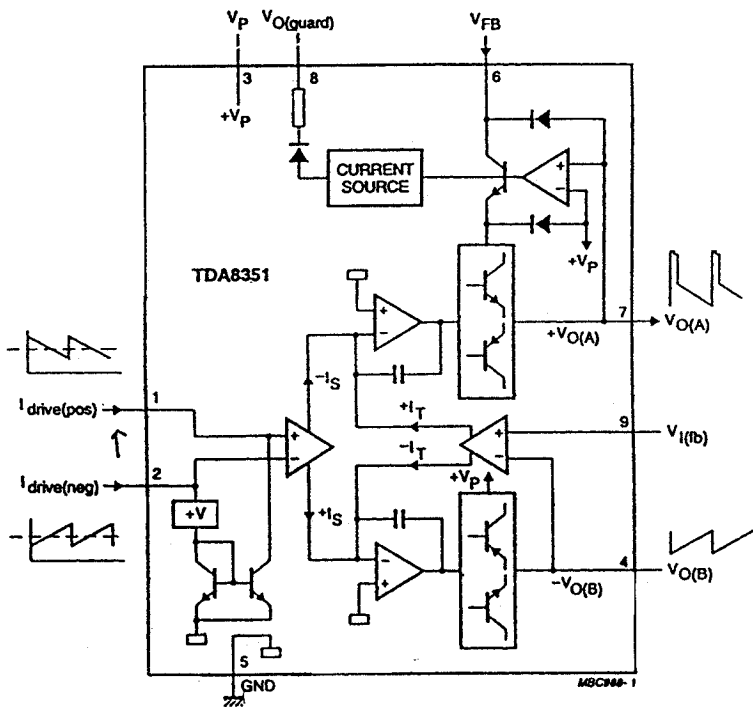


Fig.2 Block diagram.